



Product Specification Sheet

HLXDXX1XL-CD40

RoHS Compliant 10Gbps 40km DWDM XFP Optical Transceiver

SHENZHEN HAILI LINK CO., LTD

www.hilinktech.com



Product Features

- Supports 9.95 to 11.3Gb/s bit rates
- Duplex LC Connector
- Hot-pluggable XFP footprint
- Temperature-stabilized DWDM EML transmitter and PIN receiver
- 100GHz ITU Grid, C-Band
- Applicable for 40km SMF connection
- Low power consumption, < 2.0W
- Digital Diagnostic Monitor Interface
- Operating case temperature:
Commerical: 0 to 70 °C

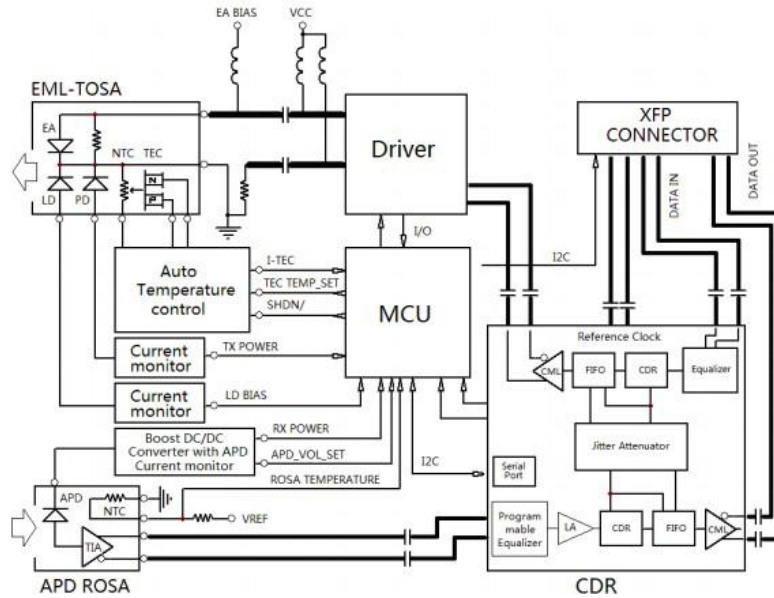
Applications

- DWDM 10G SONET/SDH
- DWDM, IEEE 10GBASE-ER based Ethernet
- ITU G.709 / OTN FEC applications
- Other optical link

Product Descriptions

Hilink HLXDXX1XL-CD40 is DWDM XFP Transceiver exhibits excellent wavelength stability, supporting operation at 100GHz channel, cost effective module. It is designed for 10G DWDM SDH, 10GBASE-ZR and 10G Fiber- Channel applications. The transceiver consists of two sections: The transmitter section incorporates a colded EML laser. And the receiver section consists of a PIN photodiode integrated with a TIA. All modules satisfy class I laser safety requirements. The DWDM XFP transceiver provides an enhanced monitoring interface, which allows real-time access to device operating parameters such as transceiver temperature, laser bias current, transmitted optical power, received optical power and transceiver supply voltage.

Functional Diagram



C-band λ c Wavelength Guide Descriptions

Chann#	Product Code	Frequency (THz)	Cent Wavelength (nm)
17	HLXD171XL-CD40	191.7	1563.86
18	HLXD181XL-CD40	191.8	1563.05
19	HLXD191XL-CD40	191.9	1562.23
20	HLXD201XL-CD40	192.0	1561.42
21	HLXD211XL-CD40	192.1	1560.61
22	HLXD221XL-CD40	192.2	1559.79
23	HLXD231XL-CD40	192.3	1558.98
24	HLXD241XL-CD40	192.4	1558.17
25	HLXD251XL-CD40	192.5	1557.36
26	HLXD261XL-CD40	192.6	1556.55
27	HLXD271XL-CD40	192.7	1555.75
28	HLXD281XL-CD40	192.8	1554.94
29	HLXD291XL-CD40	192.9	1554.13
30	HLXD301XL-CD40	193.0	1553.33
31	HLXD311XL-CD40	193.1	1552.52
32	HLXD321XL-CD40	193.2	1551.72
33	HLXD331XL-CD40	193.3	1550.92
34	HLXD341XL-CD40	193.4	1550.12
35	HLXD351XL-CD40	193.5	1549.32
36	HLXD361XL-CD40	193.6	1548.51
37	HLXD371XL-CD40	193.7	1547.72
38	HLXD381XL-CD40	193.8	1546.92
39	HLXD391XL-CD40	193.9	1546.12
40	HLXD401XL-CD40	194.0	1545.32
41	HLXD411XL-CD40	194.1	1544.53
42	HLXD421XL-CD40	194.2	1543.73
43	HLXD431XL-CD40	194.3	1542.94



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44	HLXD441XL-CD40	194.4	1542.14
45	HLXD451XL-CD40	194.5	1541.35
46	HLXD461XL-CD40	194.6	1540.56
47	HLXD471XL-CD40	194.7	1539.77
48	HLXD481XL-CD40	194.8	1538.98
49	HLXD491XL-CD40	194.9	1538.19
50	HLXD501XL-CD40	195.0	1537.40
51	HLXD511XL-CD40	195.1	1536.61
52	HLXD521XL-CD40	195.2	1535.82
53	HLXD531XL-CD40	195.3	1535.04
54	HLXD541XL-CD40	195.4	1534.25
55	HLXD551XL-CD40	195.5	1533.47
56	HLXD561XL-CD40	195.6	1532.68
57	HLXD571XL-CD40	195.7	1531.90
58	HLXD581XL-CD40	195.8	1531.12
59	HLXD591XL-CD40	195.9	1530.33
60	HLXD601XL-CD40	196.0	1529.55
61	HLXD611XL-CD40	196.1	1528.77

Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Unit	Note
Supply Voltage	Vcc	-0.5	4.0	V	
Storage Temperature	Ts	-40	85	°C	
Relative Humidity	RH	0	85	%	

Note: Stress in excess of the maximum absolute ratings can cause permanent damage to the transceiver.

General Operating Characteristics

Parameter	Symbol	Min.	Typ	Max.	Unit	Note
Data Rate	DR	9.95	10.3125	11.3	Gb/s	
Supply Voltage	Vcc	3.13	3.3	3.47	V	
Supply Current	Iccs			500	mA	
Operating Case Temp.	Tc	0		70	°C	

Electrical Characteristics (T_{OP(C)} = 0 to 70 °C, V_{CC} = 3.13 to 3.47 V)

Parameter	Symbol	Min.	Typ	Max.	Unit	Note
Transmitter						



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Differential data input swing	$V_{IN,PP}$	120		820	mv _{pp}	1
Transmit Disable Voltage	V_D	2.0		V_{CC}	V	
Transmit Enable Voltage	V_{EN}	V _{ee}		V _{ee} +0.8		
Input differential impedance	R_{in}		100		Ω	
Receiver						
Differential data output swing	$V_{out,pp}$	300		850	mv _{pp}	2
Output rise time and fall time	T_r, T_f	28			Ps	3
LOS asserted	V_{LOS_F}	$V_{CC}-0.8$		V_{CC}	V	4
LOS de-asserted	V_{LOS_N}	V _{ee}		V _{ee} +0.8	V	4

Notes:

1. Connected directly to TX data input pins. AC coupling from pins into laser driver IC.
2. Into 100 Ω differential termination.
3. 20 – 80%. Measured with Module Compliance Test Board and OMA test pattern. Use of four 1's and four 0's sequence in the PRBS 9 is an acceptable alternative.
4. LOS is an open collector output. Should be pulled up with 4.7k Ω – 10k Ω on the host board. Normal operation is logic 0; loss of signal is logic 1.

Optical Characteristics ($T_{OP(C)} = 0$ to 70 °C, $V_{CC} = 3.13$ to 3.47 V)

Parameter	Symbol	Min.	Typ	Max.	Unit	Note
Transmitter						
Operating Wavelength	λ_c	1528.77		1563.86	nm	
Center Wavelength (End of Life)	λ_{c_EOL}		$\lambda_c \pm 100\text{pm}$			
Ave. output power (Enabled)	P_{AVE}	-1		2	dBm	1
Side-Mode Suppression Ratio	SMSR	30			dB	
Extinction Ratio	ER	8.2			dB	
RMS spectral width	$\Delta\lambda$			1	nm	
Rise/Fall time (20%~80%)	T_r/T_f			45	ps	
Dispersion penalty	T_{DP}			3.9	dB	
Relative Intensity Noise	R_{IN}			-128	dB/Hz	
Output Optical Eye	IEEE 802.3-2005 Compliant					
Receiver						
Operating Wavelength	λ	1270		1610	nm	
Receiver Sensitivity	P_{SEN1}			-16	dBm	2
Overload	P_{AVE}	0.5			dBm	
LOS Assert	P_a	-30			dBm	
LOS De-assert	P_d			-18	dBm	
LOS Hysteresis	P_d-P_a	0.5			dB	

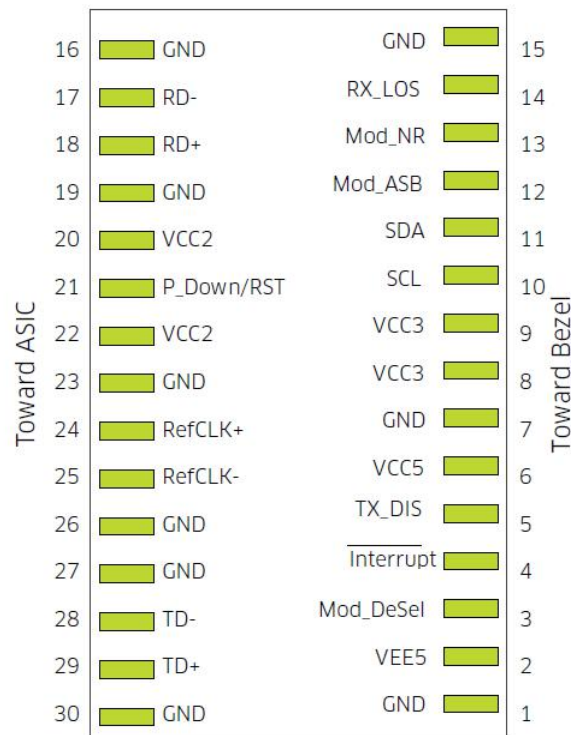
Notes:

1. Average power figures are informative only, per IEEE 802.3ae.
2. Measured with worst ER=8.2@10.3125G; BER<10⁻¹²; 2³¹-1 PRBS.



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PIN DEFINITIONS AND FUNCTIONS



PIN #	Name	Function	Name/Description	Notes
1		GND	Module Ground	1
2		VEE5	Optional -5.2V Power Supply (Not required)	
3	LVTTL-I	MOD_DESEL	Module De-select; When held low allows the module to respond to 2-wire serial interface	
4	LVTTL-O	INTb	Interrupt; Indicates presence of an important condition which can be read via the 2-wire serial interface	2
5	LVTTL-I	TX_DIS	Transmitter Disable; Turns off transmitter laser output	
6		VCC5	+5V Power Supply (Not required)	
7		GND	Module Ground	1
8		VCC3	+3.3V Power Supply	
9		VCC3	+3.3V Power Supply	
10	LVTTL-I/O	SCL	2-Wire Serial Interface Clock	2
11	LVTTL-I/O	SDA	2-Wire Serial Interface Data Line	2
12	LVTTL-O	MOD_Abs	Indicates Module is not present. Grounded in the Module	2
13	LVTTL-O	MOD_NR	Module Not Ready; Indicating Module Operational Fault	2
14	LVTTL-O	RX_LOS	Receiver Loss Of Signal Indicator	2
15		GND	Module Ground	1
16		GND	Module Ground	1
17	CML-O	RDN	Receiver Inverted Data Output	
18	CML-O	RDP	Receiver Non-Inverted Data Output	
19		GND	Module Ground	1
20		VCC2	+1.8V Power Supply (Not required).	
21	LVTTL-I	P_DOWN/RST	Power down; When high, requires the module to limit power consumption to 1.5W or below. 2-Wire serial interface must be functional in the low power mode.	



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21	LVTTL-I	P_DOWN/RST	Reset; The falling edge initiates a complete reset of the module including the 2-wire serial interface, equivalent to a power cycle.	
22		VCC2	+1.8V Power Supply (Not required)	
23		GND	Module Ground	1
24	PECL-I	REFCLKP	Not used, internally terminated to 50ohm (100ohm diff).	3
25	PECL-I	REFCLKN	Not used, internally terminated to 50ohm (100ohm diff).	3
26		GND	Module Ground	1
27		GND	Module Ground	1
28	CML-I	TDN	Transmitter Inverted Data Input	
29	CML-I	TDP	Transmitter Non-Inverted Data Input	
30		GND	Module Ground	1

Notes:

1. Module circuit ground is isolated from module chassis ground within the module.
2. Open collector; should be pulled up with 4.7k – 10k ohms on host board to a voltage between 3.15V and 3.6V.
3. Reference Clock input is not required.

Management Interface

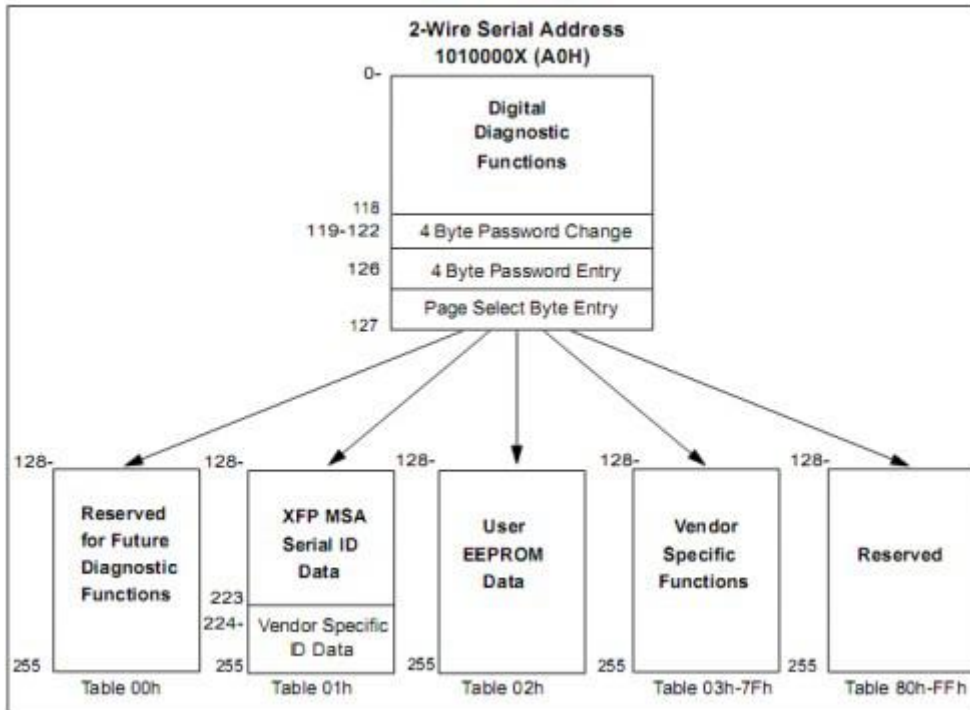
The transceivers provide serial ID memory contents and diagnostic information about the present operating conditions by the 2-wire serial interface (SCL, SDA).

The Module provides diagnostic information about the present operating conditions. The transceiver generates this diagnostic data by digitization of internal analog signals. Alarm/warning threshold data is written during device manufacture. Received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring all are implemented.

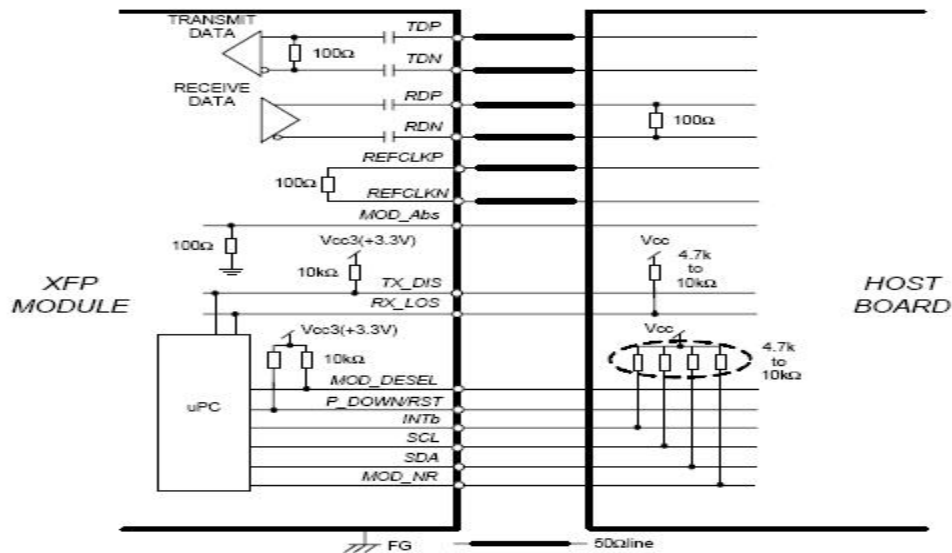
The digital diagnostic memory map specific data field defines as following.



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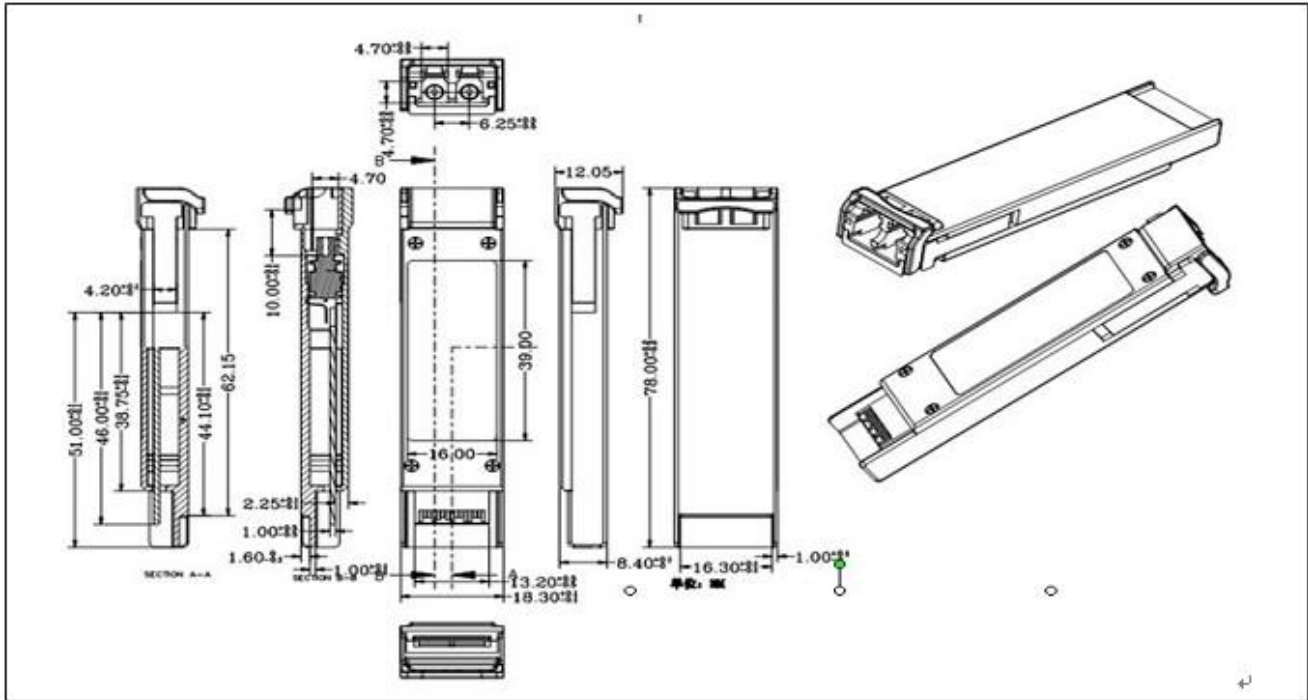
TYPICAL INTERFACE CIRCUIT



Package Dimensions



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